



Verilog digital system design tutorial

By XIA YU WEN

paperback. Book Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment. Pages Number: 477 Publisher: Beijing Aerospace University Press Pub. Date :2008-06. Verilog Digital System Design Guide (2nd edition) about the 90 years since the 20th century In the United States and other advanced industrialized countries to gradually extend the use of hardware description language (VerilogHDL) modeling, simulation and design of complex digital logic integrated circuits and systems methods and techniques. Verilog Digital System Design Guide in the content calculated from the algorithm and the basic concept. about how the hard-wired logic circuits to complex digital logic systems approach. Verilog Digital System Design Guide is divided into four parts the first part of a total of eight chapters. namely Verilog Digital Design Basics. as undergraduates introductory material. The second part of Chapter 10. namely the design and verification papers. as senior undergraduate or graduate student studying digital system design reference. The third part is practice articles. providing a total of 12 hands-on examples and experiments The fourth part is the syntax of articles that Verilog Hardware Description Language Reference Manual; IEEEVerilog13642001 standard profiles to reflect the latest changes in Verilog...

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